

CLAIMS

1. An apparatus comprising:

a circuit comprising one or more inputs configured to provide a device identification (ID) of one or more different device IDs, wherein said one or more inputs allow said circuit to be implemented with one of said one or more different device IDs.

2. The apparatus according to claim 1, wherein said device identification ID comprises a soft code.

3. The apparatus according to claim 1, wherein said circuit comprises a JTAG controller.

4. The apparatus according to claim 1, wherein each of said one or more different device IDs comprise a configuration of said circuit.

5. The apparatus according to claim 1, wherein said device identification ID can be configured after fabrication of said apparatus.

6. The apparatus according to claim 1, wherein said circuit comprises:

a logic circuit configured to receive said one or more inputs;

5 a multiplier configured to receive an output of said logic circuit; and

a memory element configured to receive an output of said multiplexer.

7. The apparatus according to claim 6, wherein said multiplexer is further configured to receive an input signal and a shift signal.

8. The apparatus according to claim 7, wherein said logic circuit comprises a logic gate.

9. The apparatus according to claim 1, wherein said circuit is implemented within a FIFO memory.

10. The apparatus according to claim 1, wherein said one or more inputs comprise mark options.

11. The apparatus according to claim 1, wherein said one or more inputs comprise configuration input pins.

12. The apparatus according to claim 1, wherein said circuit comprises a JTAG device compliant with the IEEE standard 1149.1.

13. An apparatus comprising:  
means for receiving one or more inputs; and  
means for providing a device identification (ID) of one or more different device IDs, wherein said one or more inputs allow implementation of said one or more different device IDs.

14. A method for multiple device identifications (IDs) comprising the steps of:

(A) receiving one or more inputs; and

5 (B) providing a device identification (ID) of one or more different device IDs, wherein said one or more inputs allow implementation of said one or more different device IDs.

15. The method according to claim 14, wherein said device identification ID comprises a soft code.

16. The method according to claim 14, wherein each of said one or more different device IDs comprise a circuit configuration.

17. The method according to claim 14, wherein said device identification can be configured after fabrication.

18. The method according to claim 14, wherein said one or more inputs comprise mark options.

19. The method according to claim 14, wherein said one or more inputs comprise configuration input pins.

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